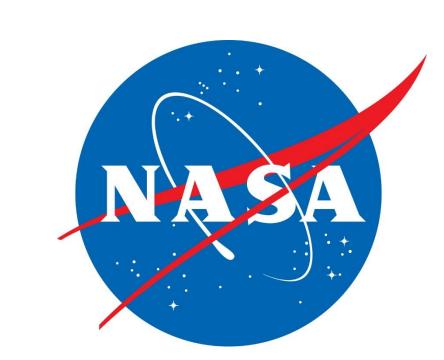
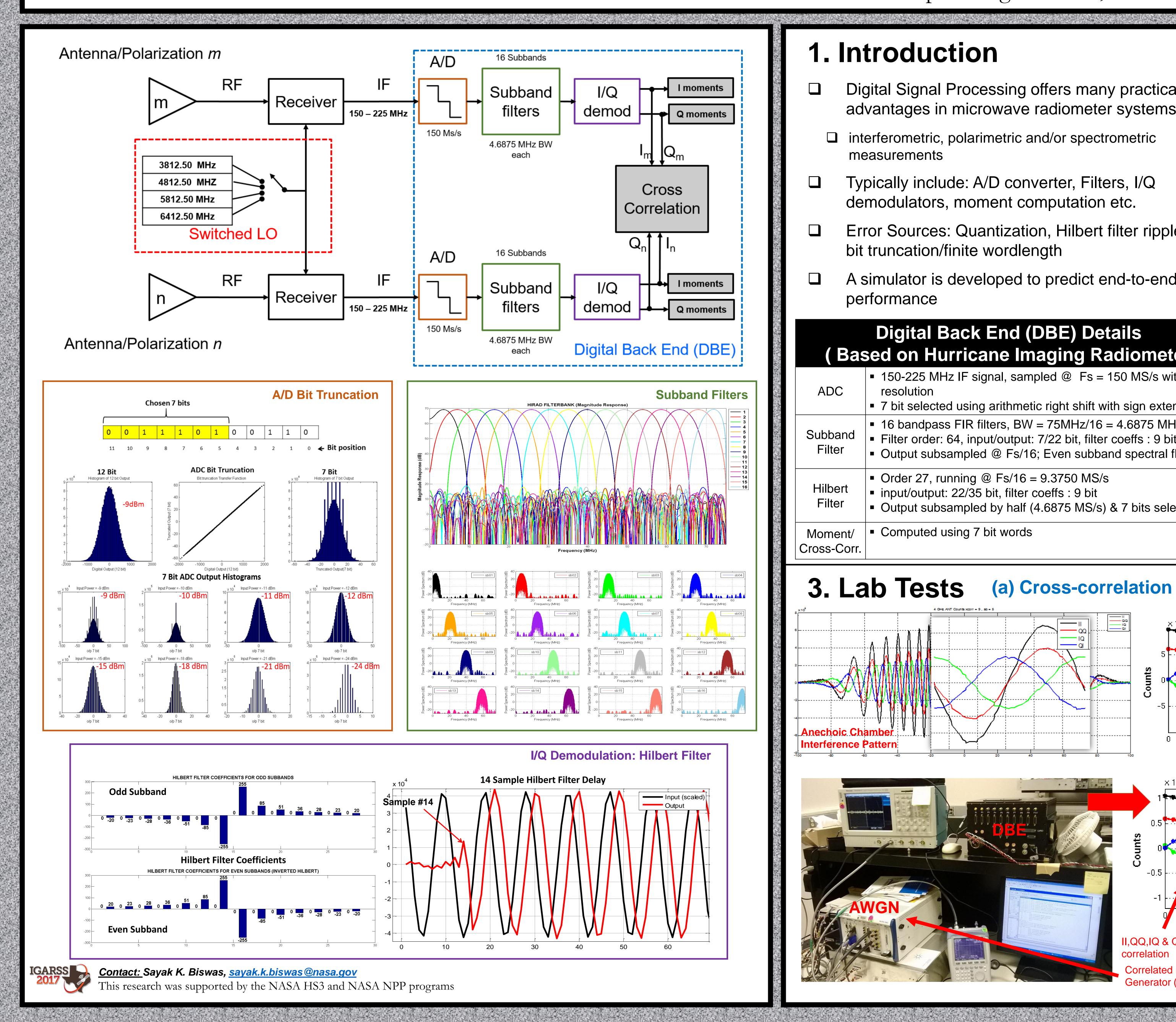


Characteristic of a digital correlation radiometer back-end with finite wordlength

¹Sayak K. Biswas, ²David W. Hyde, ²Mark W. James and ²Daniel J. Cecil



¹Universities Space Research Association, Huntsville, Al 35805 ²NASA Marshall Space Flight Center, Huntsville, Al 35805



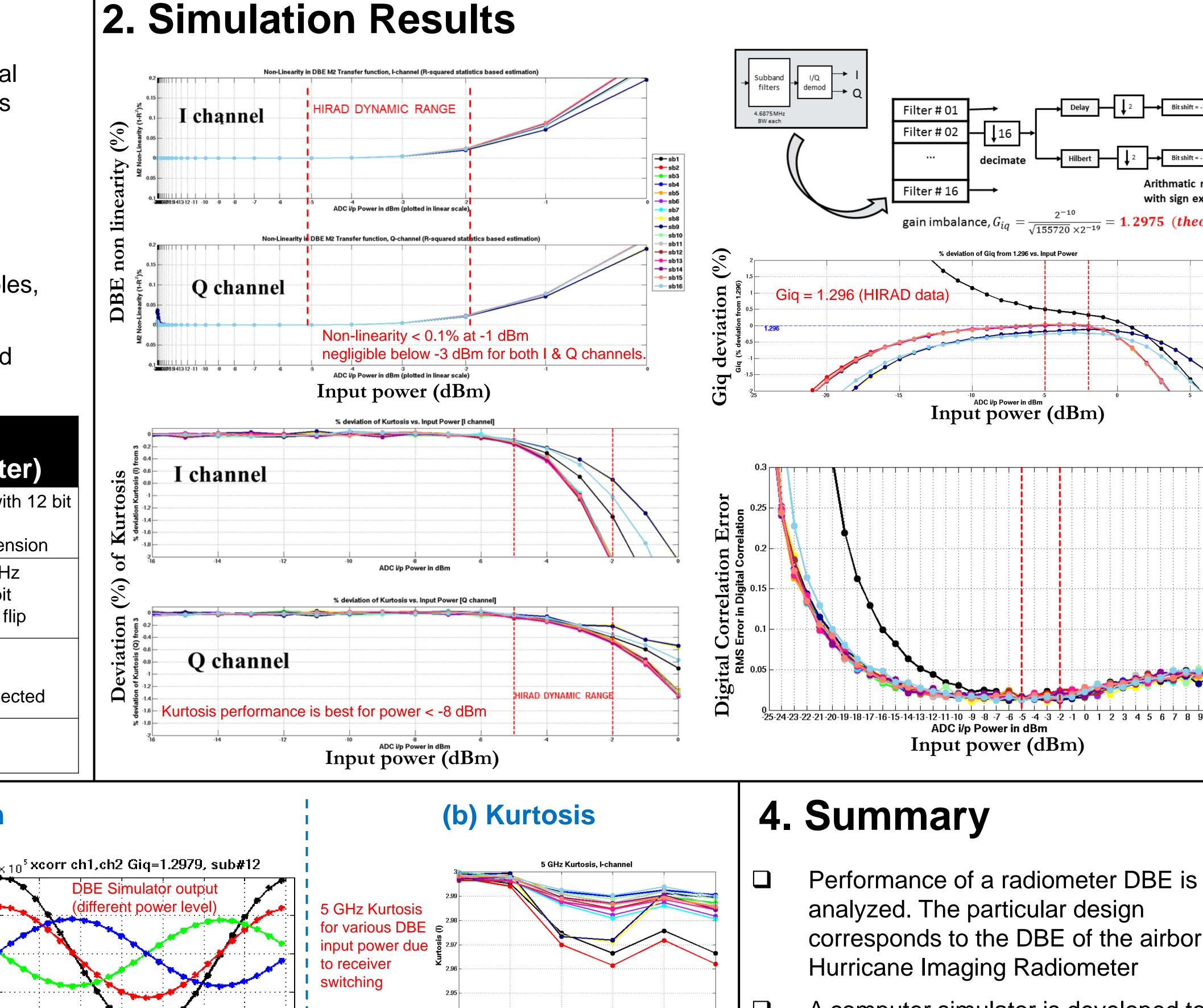
. Introduction Digital Signal Processing offers many practical advantages in microwave radiometer systems interferometric, polarimetric and/or spectrometric measurements Typically include: A/D converter, Filters, I/Q demodulators, moment computation etc. Error Sources: Quantization, Hilbert filter ripples, bit truncation/finite wordlength A simulator is developed to predict end-to-end performance Digital Back End (DBE) Details (Based on Hurricane Imaging Radiometer) ■ 150-225 MHz IF signal, sampled @ Fs = 150 MS/s with 12 bit ADC 7 bit selected using arithmetic right shift with sign extension ■ 16 bandpass FIR filters, BW = 75MHz/16 = 4.6875 MHz Subband • Filter order: 64, input/output: 7/22 bit, filter coeffs: 9 bit Output subsampled @ Fs/16; Even subband spectral flip Order 27, running @ Fs/16 = 9.3750 MS/s input/output: 22/35 bit, filter coeffs: 9 bit Output subsampled by half (4.6875 MS/s) & 7 bits selected Computed using 7 bit words Moment/ Cross-Corr.

Phase Angle (θ) in deg

xcorr ch1,ch2 Giq=1.2895, sub#12

Phase Angle (θ) in deg

Correlated noise generated using Arbitrary Waveform



5 GHz Kurtosis (with 10dB attenuator), I-channel

ant ndant
Receiver Switching State

Power: -2.15 dBm

5 GHz WiFi

Interference

sb7
sb8
sb9
sb10
sb11
sb12
sb13
sb14
sb15
sb15

- analyzed. The particular design corresponds to the DBE of the airborne
- A computer simulator is developed to analyze effect of input power on various DBE output products
- 2nd moment non-linearity is found to be negligible in the expected input signal dynamic range
- Observed scaling between I and Q channels and the scaling among crosscorrelation signals are verified by the simulator
- Kurtosis sensitivity can be improved by lowering the input power – predicted by the simulator and verified in the lab